

JULY 10/03673
11/02/01

U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NUM 10003673	FILING DATE 11/02/2001	CLASS 714	SUBCLASS 720	GAU 2/33 2184	EXAMINER <i>[Signature]</i>
**APPLICANTS: Felber Norbert; Kaeslin Hubert; <i>[Signature]</i> J. Kerveros					
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** FOREIGN APPLICATIONS VERIFIED: <i>[Signature]</i> 3/18/04 EUROPEAN PATENT OFFICE (EPO) 00124076.1 11/06/2000					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed 35 USC 119 conditions met Verified and Acknowledged Examiners's initials		<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <i>[Signature]</i> 3/18/04		ATTORNEY DOCKET NO PHCH000024	
TITLE : Method for testing integrated circuit					
<small>U.S. DEPT. OF COMM. / PAT. & TM-PTO-436L (Rev. 12-94)</small>					

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED		
			Total Claims	Print Claim for O.G.	
ISSUE FEE			DRAWING		
Amount Due	Date Paid	Primary Examiner	Sheets Drwg.	Figs. Drwg.	Print Fig.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	Application Examiner		
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